

**Abstract**

A telecommunications chassis, module, and repeater circuit for use with signals having data rates including STM-1 (155.52 megabits per second) are disclosed. The chassis provides structures for establishing shielding and heat dissipation for the circuitry modules it contains including an outer and an inner Faraday box with an integrated ventilation pattern for circulating air. The module provides its own structures for establishing shielding and heat dissipation including a Faraday box and a ventilation pattern. The repeater circuit provides the ability to bridge a data signal between a monitor jack of one device and a higher signal level input jack of another device through multiple amplification stages and circuit board structures. The telecommunications chassis, module, and repeater circuit can be used in conjunction.

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